

A.

For your ECE186 laboratory project( i.e. 68000-based single board computer) manually generate a TRUTH TABLE representing the input/output conditions of your address decoder for the following situations: THIS IS DUE THURSDAY MARCH 21, 2002, IN LAB.

1. AS\* is invalid; all other inputs are 'don't cares'.
2. LDS\* is invalid; all other inputs are 'don't cares'.
3. Reading from EPROM.
4. Attempting to write to EPROM.
5. Reading from SRAM.
6. Writing to SRAM.
7. Reading from DIP SWITCH.
8. Attempting to "write to DIP SWITCH"
9. Writing to LED
10. Attempting to "Read from LED"
11. Reading from A/D
12. Writing to A/D
13. Reading form D/A
14. Writing to D/A
15. Reading from the UART
16. Writing to the UART

B.

Write a three-page narrative plus appropriate computer-generated diagrams to serve as your laboratory progress report for the single-board computer. The report is due on Thursday, April 11. The report, which should be computer-generated, should address the following items ( see page 'd' of lecture notes):

1. The Cover Sheet.
2. Abstract/Summary
3. Introduction
4. Body of report.( items a thru. d.; also item g for preliminary results.
5. APPENDICES: MEMORY MAP, ADDRESS DECODING TABLE, ADDRESS DECODER, SCHEMATIC( UP TO MEMORY, DIP SWITCH AND LED)