

ECE186 SPRING 2002 WK. 7 WEEK OF MARCH 11, 2002
TODAY'S DATE: MONDAY, March 11, 2002.

LABORATORY-RELATED ASSIGNMENT:

1. From the ALTERA Corporation's Website, download the latest version of the student edition of the MAXPLUS II software on your home computer. Follow the directions on the Altera University Programs' section of the Website for installing the software and obtaining the license. **DUE IN LAB. THURSDAY MARCH 14, 2002.**
2. Using the two Lab. Reports at the end of your Lecture/Lab. Notes as a guide and reference, do the following: { **THE FIRST FIVE ITEMS BELOW DUE 3/18/02** }
 - Develop a well-labeled Block Diagram for the 68EC000(8-bit data bus mode) based single board computer for your ECE186 project.
 - Generate a well labelled Memory Map for the project.
 - Provide an Address Decoding Table.
 - Draw the logic diagram for the address decoding Logic using a 74138 for the core of the decoding circuit.
 - Using the schematic capture option of the Altera MAXPLUS II software, draw the address decoder circuitry in the MAXPLUS II CAD environment.
 - Simulate the address decoder circuit to establish proper functioning of your circuit.