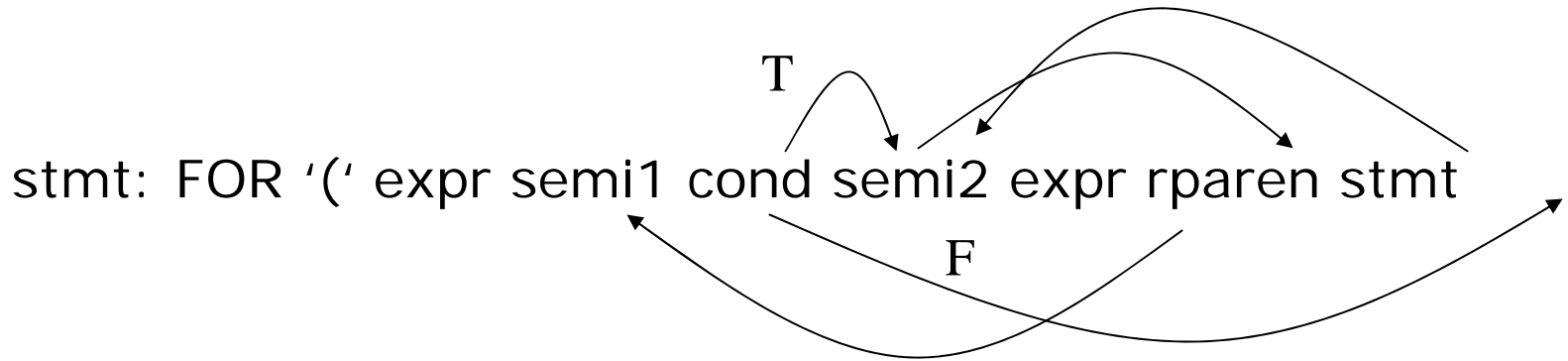


For Statement



{ branch semi2; fixup cond with end; fixup rparen with semi1; fixup semi2 with rparen; }

semi1: ';' { pick off address; }

semi2: ';' { emit branch, STOP (fixup later); pick off address; }

rparen: ')' { emit branch, STOP (fixup later); pick off address; }