EECE-315 Exercise #4

Chapter 6 Problems

1. D6.15
2. 6.26
3. 6.44
4. 6.67
5. For the circuit shown in Figure P6.16, the transistor parameters are $\beta = 150$ and $V_A = \infty$. Design a bias-stable circuit to achieve the maximum undistorted swing in the output voltage if the instantaneous C-E voltage is to remain in the range $1\ V \leq v_{CE} \leq 8\ V$.