ACRONYMS

ALU: Arithmetic Logic Unit
CISC: Complex Instruction Set Computing
DRAM: Dynamic Random Access Memory
EPIC: Explicitly Parallel Instruction Computing
HP-UX: Hewlett Packard Unix
IA-64: Intel Architecture 64
ILP: Instruction Level Parallelism
ISA: Instruction Set Architecture
MCB: Memory Conflict Buffer
POE: Plan Of Execution
RISC: Reduced Instruction Set Computing
VLIW: Very Long Instruction Word