

Welcome to my web page.

Being a Full Professor, I do enjoy my teaching here at California State University, Chico. Three ECE students who took **EECE 675 Electromagnetic Compatibility** that I created and taught in the spring semester of 2001 participated in an **International Student Design Competition** project sponsored by the IEEE EMC Society.

I used my knowledge and many year research experiences in High-Speed Digital Systems Design, Design for Testability, and Electronic Design Automation to guide and direct the three ECE students through the **International Student Design Competition** project.

On following my advices and directions, finally the students won the first place of **the 2001 Best Student Design, IEEE EMC Society**, Montreal, Canada, Aug. 14-18, 2001, which is an international competition with 37 universities participated in from 7 countries around the world.

I created a new senior and graduate course, **EECE 447 VLSI Design**, which was offered to Electrical and Computer Engineering graduate and senior undergraduate students in the spring semester of 2002. The **EECE 447 VLSI Design** is the first course in the ECE Department to teach students Application Specific Integrated Circuit (ASIC) designs. The reason I created this course is because there is a big demand for ASIC design engineers in industries.

Since Intel has announced the Pentium 4 (2.8 GHz) microprocessor it will be vitally necessary for our graduates to know how to design high-frequency electronic circuits. I am teaching **EECE 615 High-Frequency Design Techniques** this fall semester of 2004. I apply such a theme as **“If the Speed of Light IS Not Fast Enough, What Should Be Done In Electronic Circuit Designs?”** in teaching this class.

The **EECE 643 Computer-Aided Circuit Engineering** has been taught by me since the spring semester of 2001. The theme I pursued in this class has been **"Electronic Design Automation."** Since my research interests are in Digital Systems Design and Electronic Design Automation, I mainly teach and guide students to explore how to design and optimize digital circuits and system designs using Design Automation techniques and tools, and the methodologies how to develop such Design Automation tools as MAX+PLUS II, Verilog, and PSPICE. The topic of **“Very High Speed Integrated Circuit Hardware Description Language (VHDL) Programming”** was extensively explored in teaching **EECE 643**.

The undergraduate courses I have taught are **EECE 144, EECE 311, ECE 315, EECE 343, and EECE 447**. One thing I always do in a course that covers a subject as

general and broad as this one is to pursue a central theme throughout the semester, a theme that can not only help the students to relate to various applications in the real world, but also serve as a focus for everything we do in class.

In general, my teaching philosophy for Electrical and Computer Engineering students is implemented with the five aspects: Theory, Examples, Hardware Experiments, Software Simulation, and Design Projects.

Again, welcome to my web page and I hope that you will enjoy your learning experience here at the Department of Electrical and Computer Engineering, and have a best placement you are dreaming now in the near future.