

Teaching

My teaching interests are in the area of digital systems design and applications, computer architecture, microprocessor systems as well as processors for digital signal processor applications. This has included logic design as well as design for ASICS with VHDL and system integration with PLDs and FPGAs. I am particularly interested in the hardware/software design interface with general as well as special purpose microprocessors. I am particularly interested in laboratory-based hands-on design courses.

Some of the courses I have taught here at Chico State are:

1. EECE 144 Logic Design Fundamentals
2. EECE 135 Algorithms and Programs for Engineers
3. EECE 320 System Architecture and Performance.
4. EECE 344 Digital Systems Design (Microprocessor interfacing).
5. EECE 444 Microprocessor Systems Design
6. EECE 490A and B Senior Project Planning and Design/Construction/Testing/Documentation
7. EECE 669 Topics in Digital Signal Processing.