

Dr. Hede Ma's Publications:

1. H. Ma, "CMOS Integrated Circuit Designs with LASI," In Proc. 2002 COC National Conference, Myrtle Beach, South Carolina, Nov. 10 – 13, 2002.
2. H. Ma, "Automatic Fault Isolation for reliable Design of Digital Systems," In Proc. 12th International Conference on Electronics, Communications and Computers, IEEE, Acapulco, Mexico, Feb. 25 – 28, 2002, pp.116-120.
3. H. Ma, "Gery Digital System Testing," In Proc. 12th International Conference on Electronics, Communications and Computers, IEEE, Acapulco, Mexico, Feb. 25 – 28, 2002, pp.105-110.
4. H. Ma, "PCB Design with EMI Mitigation Techniques Using OrCad 9.2 and National Semiconductor's Web Bench," In Proc. 2001 COC National Conference, Myrtle Beach, South Carolina, Nov. 11 – 14, 2001.
5. H. Ma, "Design for Testable Four-Channel Data Transfer Controller Using VHDL," In Proc. 11th International Conference on Electronics, Communications and Computers, IEEE, University of Americas- Puebla, Cholula, Puebla, Mexico, Feb. 22 – 24, 2001, pp.129-132.
6. H. Ma, "Software Test: Theory and Practice," In Proc. 11th International Conference on Electronics, Communications and Computers, IEEE, University of Americas- Puebla, Cholula, Puebla, Mexico, Feb. 22 – 24, 2001, pp.141-143.
7. H. Ma, "Digital system design using cutting-edge design automation software and field-programmable gate array chips," In Proc. 2000 COC National Conference, Myrtle Beach, South Carolina, Nov. 12 – 15, 2000, pp.58-62.
8. H. Ma, "Development of a new software Engineering program and curriculum," The 60th ASEE Midwest Conference, Winona, MN, Oct. 8-10, 1998.
9. H. Ma, "Applications of Design Automation Software for Rapid Prototyping of Digital Systems," The 60th ASEE Midwest Conference, Winona, MN, Oct. 8-10, 1998.
10. H. Ma, "Bus Expansion Card Design in Microcomputer Architecture and Interfacing," In Proc. The 59th ASEE Midwest Conference, Iowa City, IA, Oct. 9-11, 1997, pp.111-116.
11. H. Ma, "Engineering Consulting and Engineering Education," In Proc. The 59th ASEE Midwest Conference, Iowa City, IA, Oct. 9-11, 1997, pp.41-45.
12. H. Ma, "Functions of Final Projects and Project Teams in Computer Engineering Education," In Proc. The 59th ASEE Midwest Conference, Iowa City, IA, Oct. 9-11, 1997, pp.21-26.
13. H. Ma, "The Necessity of A New Engineering Course - Patent It Yourself," In Proc. The ASEE/PSW 1997 Conference, San Luis Obispo, CA, Mar. 14-15, 1997, pp.129-133.
14. H. Ma, "Altera FPGA Applications and Advanced Digital Design," In Proc. The 58th ASEE Midwest Conference, Fargo, ND, Sep. 3-5, 1996, pp.31-37.
15. H. Ma, "Engineering Laboratories and Engineering Education," In Proc. The 58th ASEE Midwest Conference, Fargo, ND, Sep. 3-5, 1996, pp.51-55.
16. H. Ma, "Pattern recognition using Boltzmann machine," In Proc. IEEE Southeastcon'95, Raleigh, NC, March 26-29, 1995, pp.23-29.
17. H. Ma, "A customer-oriented test - estimation test," In Proc. 1994 IEEE Southeastcon'94, Miami, FL., April 11-13, 1994, pp.241-245.
18. H. Ma, "A new testing philosophy - differential test," In Proc. 1993 IEEE Southeastcon'93, Charlotte, NC., April 5-7, 1993, pp.6-9.
19. Y. Liu and H. Ma, "Macrocode for error detection and correction in mass memories," In Proc. 1993 IEEE Southeastcon'93, Charlotte, NC., April 5-7, 1993, pp.201-207.
20. Y. Liu and H. Ma, "A simulation of macrocode for error detection and correction in mass memories," In Proc. 1993 International Simulation Technology Conference, Clear Lake, Texas, Nov. 4-6, 1992, pp.139-144.
21. H. Ma, "An efficient simulator for automatic isolation of intermittent faults in digital systems," In Proc. The 40th Computer Simulation Conference, Reno, Nevada, July 27-30, 1992, pp.250-254.
22. Y. Liu and H. D. Ma, "Image analysis using class 2 and class 5A dynamical systems," In Proc. The 45th Conference of The Society for Imaging Science and Technology, East Rutherford, NJ., May 10-15, 1992, pp.201- 204.
23. Y. Liu and H. Ma, "Image analysis using class 3 and class 5B dynamical systems," In Proc. The 45th Conference of The Society for Imaging Science and Technology, East Rutherford, NJ., May

- 10-15, 1992, pp.205- 208.
24. H. Ma and Y. Liu, "Automatic fault isolation techniques for digital systems," In Proc. IEEE Southeastcon'92, Birmingham, AL, April 12-15, 1992, pp.335-338.
 25. H. Ma and Y. Liu, "A testable design to test pattern sensitive faults efficiently for semiconductor random access memories," In Proc. IEEE Southeastcon'92, Birmingham, AL, April 12-15, 1992, pp.339-342.
 26. Y. Liu and H. Ma, "A comparison of two learning philosophies," In Proc. IEEE Southeastcon'92, Birmingham, AL, April 12-15, 1992, pp.247-254.
 27. Y. Liu and H. Ma, "Comparison between image analysis using class 2 and class 3 dynamical systems," Image Processing Algorithms and techniques III, James R. Sullivan, Editors, Proc. SPIE 1657, 1992, pp.461-475.
 28. H. Ma and Y. Liu, "Pattern recognition using ω -orbit finite automata," In Proc. Visual Communications and Image Processing'91: Image processing, Boston, MA, Nov. 11-13, 1991, pp.226-240.
 29. H. Ma and Y. Liu, "Algorithms for moving object pattern recognition using ω -orbit finite automata," In Proc. 1991 International Simulation Technology Conference, Orlando, FL, Oct. 21-23, 1991, pp.552-558.
 30. Y. Liu and H. Ma, "Pattern recognition using the third and the fifth classes of dynamical systems," In Proc. The Sixth International Symposium on Methodologies for Intelligent Systems, (Poster Session), Charlotte, NC, Oct. 16-19, 1991, pp.83-94.
 31. Y. Liu and H. Ma, "A parallel pattern recognition approach," In Proc. The 4th International Conference on Parallel and Distributed Computing Systems, Washington D.C., Oct. 8-11, 1991, pp.255-260.
 32. H. Ma and Y. Liu, "Efficient placement of error checkers in VLSI systems," In Proc. Test Engineering Conference, Atlanta, GA, June 25-27, 1991, pp.103-111.
 33. H. Ma and Y. Liu, "Self-verification of semiconductor random access memories," In Proc. Test Engineering Conference, Atlanta, GA, June 25-27, 1991, pp.113-118.
 34. Y. Liu and H. Ma, " ω -orbit finite automata and image compression," In Proc. 44th Annual Conference of the Society for Imaging Science and Technology, St. Paul, Minnesota, May 12-17, 1991, pp.489-492.
 35. H. Ma and Y. Liu, "Functional testing of digital systems described by AHDL," In Proc. 22nd Annual Conference on Modeling and Simulation, Pittsburgh, PA, May 2-3, 1991, pp.1644-1651.
 36. H. Ma and Y. Liu, "Design for diagnosable multipleoutput digital systems," In proc. IEEE VLSI Test Conference, Atlantic City, NJ, April 15-17, 1991, IEEE Computer Press, pp.204-209.
 37. Y. Liu and H. Ma, " ω -orbit finite automata for data compression," In Proc. IEEE Data Compression Conference, Snowbird, Utah, April 8-11, 1991, IEEE Computer Press, pp.166-175.
 38. H. Ma and Y. Liu, "Necessary and sufficient conditions for efficient fault isolation in digital systems," In Proc. 25th Annual Conference on Information Sciences and Systems (CISS) 1991, The Johns Hopkins University, Baltimore, MD, March 20-22, 1991, pp.868-873.
 39. H. Ma, Efficient Fault Isolation Schemes for Grey Digital Systems, Ph.D. dissertation, State University of New York, Binghamton, NY, May, 1990.
 40. S. Y. H. Su and H. Ma, "Design for Diagnosability and reliability in VLSI systems," In Proc. IEEE International Test Conference, Washington D. C., 1988, pp.888-897.
 41. S. Y. H. Su and H. Ma, "Fault isolation in grey digital systems," In Proc. IEEE International Test Conference, Washington D. C., 1988, pp.54-63.
 42. H. Ma and T. N. Rajshakahara, "A testable design of semiconductor random access memories," In Proc. 30th Midwest Conference on Circuits and Systems, Syracuse, NY, 1987, pp.540-543.
 43. H. Ma, "Livello avoidance in packet switching networks," In Proc. The Computer Conference of Shanghai, Shanghai, China, 1983.
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